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November 15, 2001

ATTORNEY DOCKET NO. 06720.0074
CUSTOMER NUMBER: 22,852

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

Re: New U.S. Patent Application
Title: ELECTROSTATIC DISCHARGE PROTECTION
FOR A MIXED-VOLTAGE DEVICE USING A STACKED-
TRANSISTOR-TRIGGERED SILICON CONTROLLED
RECTIFIER
Inventor(s): MING-DOU KER ET AL.

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application - 32 pages, including 5 independent claims and 51 claims total.
2. Drawings - 7 sheets of formal drawings (Figures 1-7).



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The filing fee is calculated as follows:

Basic Application Filing Fee					\$740	\$ 740.00
	Number of Claims		Basic	Extra Claims		
Total Claims	51	-	20	31	x \$18	558.00
Independent Claims	5	-	3	2	x \$84	168.00
[] Presentation of Multiple Dep. Claim (s)					+\$270	
Subtotal						\$ 1466.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 1466

3. A check for \$1466.00 is enclosed. The fee includes:

\$ 740.00 filing fee;
\$ 726.00 additional claim fee;

Please address all correspondence with respect to this application to:

Finnegan, Henderson, Farabow,
Garrett & Dunner, L.L.P.
1300 I Street, N.W.
Washington, D.C. 20005-3315

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for
filing the Declaration.

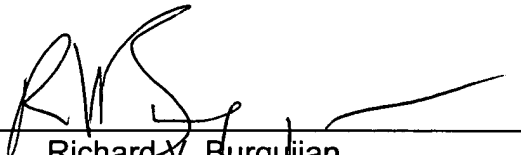
Please accord this application a serial number and filing date.

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: 
Richard V. Burgujian
Reg. No. 31,744

RVB:pwI
Enclosures